

## WEST Search History

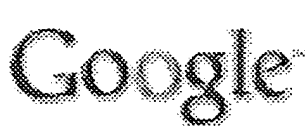




DATE: Tuesday, April 12, 2005

Hide?	Set Name	Query	Hit Count
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L20	L19 same (read or write)	143
<input type="checkbox"/>	L19	L18 same (processor)	560
<input type="checkbox"/>	L18	L17 same (secure memory)	2634
<input type="checkbox"/>	L17	L16 same (memory block)	3601
<input type="checkbox"/>	L16	L15 same (security rule)	3601
<input type="checkbox"/>	L15	L14 same (privilege mode)	38543
<input type="checkbox"/>	L14	(block of memory) same (security levels)	422419
<input type="checkbox"/>	L13	L8 same "privilege mode"	0
<input type="checkbox"/>	L12	L11 and "privilege mode"	0
<input type="checkbox"/>	L11	L10 and "security levels"	8
<input type="checkbox"/>	L10	"microcontroller" and "memory block"	1443
<input type="checkbox"/>	L9	L8 near (policy rule)	6
<input type="checkbox"/>	L8	(security level) adj (memory block)	26954
<input type="checkbox"/>	L7	security level adj memory block	3117617
<input type="checkbox"/>	L6	711/164.ccls.	326
<input type="checkbox"/>	L5	711/163.ccls.	1098
<input type="checkbox"/>	L4	713/193.ccls.	989
<input type="checkbox"/>	L3	713/202.ccls.	1140
<input type="checkbox"/>	L2	713/201.ccls.	3503
<input type="checkbox"/>	L1	713/200.ccls.	2933

END OF SEARCH HISTORY



[Web](#) [Images](#) [Groups](#) [News](#) [Froogle](#) [Local](#) [New!](#) [more »](#)

microcontroller, security rule, memory block, s

[Advanced Search](#)  
[Preferences](#)

**Web Results 1 - 10** of about **16,600** for **microcontroller, security rule, memory block, security levels, secur**

### Breaking copy protection in microcontrollers

... Each **microcontroller** executes the **algorithm** or program uploaded into its **memory**.

... Ordinary **microcontrollers** as a **rule** also have **security** protection ...

[www.cl.cam.ac.uk/~sps32/mcu\\_lock.html](http://www.cl.cam.ac.uk/~sps32/mcu_lock.html) - 49k - [Cached](#) - [Similar pages](#)

### Ross Anderson's Home Page

... Two Practical and Provably **Secure Block** Ciphers: BEAR and LION shows how to

... penetrate the smartcards and **secure microcontrollers** of the mid-1990s. ...

[www.cl.cam.ac.uk/users/rja14/](http://www.cl.cam.ac.uk/users/rja14/) - 84k - Apr 10, 2005 - [Cached](#) - [Similar pages](#)

### Embedded.com - The new Untouchables: MCUs

Increasing demands for **security** at all **levels** of society from cell phone ...

to passport services are focusing greater attention on **microcontrollers**.

[www.embedded.com/showArticle.jhtml?articleID=47902778](http://www.embedded.com/showArticle.jhtml?articleID=47902778) - 87k - [Cached](#) - [Similar pages](#)

### [PDF] Security Policy for the Security Module with CP/Q++ part of the ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... Included cryptographic **algorithms**. Cryptographic module **security levels** ...

flash **memory**, dynamic RAM, and a **microcontroller** to manage state and mediate ...

[csrc.nist.gov/cryptval/140-1/140sp/140sp345.pdf](http://csrc.nist.gov/cryptval/140-1/140sp/140sp345.pdf) - [Similar pages](#)

### [PDF] Eagle 64K Flash Module v1 FIPS140-2 Level 2 Cryptographic Module ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... In particular, this **security** policy specifies the **security rules** under which

... The AT90SC6464C is a low-power, high-performance **secure microcontroller** ...

[csrc.nist.gov/cryptval/140-1/140sp/140sp380.pdf](http://csrc.nist.gov/cryptval/140-1/140sp/140sp380.pdf) - [Similar pages](#)

[ [More results from csrc.nist.gov](#) ]

### Guardant Anti-piracy Protection | Products

... a microprogram is written into the **memory** of its **microcontroller**. ... set of properties ensures a maximum **level** of **security** against dongle emulation. ...

[www.guardant.com/products/technology.htm](http://www.guardant.com/products/technology.htm) - 40k - [Cached](#) - [Similar pages](#)

### Design For Security - Increased Design Security Through ...

... contain a 24 MHz 8-bit **microcontroller** unit (MCU); 16 kbytes of flash **memory**;

... **security** feature that promises **levels** of design **security** beyond . ...

[www.adapticom1.net/ds/Design-Security.html](http://www.adapticom1.net/ds/Design-Security.html) - 24k - [Cached](#) - [Similar pages](#)

### Smart Card Security

... These **security** mechanisms and **algorithms** provide a logical protection of the

... a system is **secure** or not depends on whether the **level** of **security** can ...

[www.javacard.org/others/sc\\_security.htm](http://www.javacard.org/others/sc_security.htm) - 21k - [Cached](#) - [Similar pages](#)

### [PDF] 1 Topics Covered

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... The Dallas 5000 series **secure microcontroller** is a CPU designed to be used with

... include MAC bytes for **blocks** of **memory**. 2.6 Lessons ...

[web.mit.edu/6.857/OldStuff/Fall97/lectures/lecture14.pdf](http://web.mit.edu/6.857/OldStuff/Fall97/lectures/lecture14.pdf) - [Similar pages](#)

[PDF] [HIPAA Compliance and Smart Cards: Solutions to Privacy and ...](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... Motorola produced the first **secure** single-chip **microcontroller** ... Defined **blocks** of **memory** must be allocated to each application ...

[www.martsoft.com/reference/healthcare/HIPAA\\_Compliance\\_and\\_Smart\\_Cards\\_FINAL.pdf](http://www.martsoft.com/reference/healthcare/HIPAA_Compliance_and_Smart_Cards_FINAL.pdf) - [Similar pages](#)

Google

Result Page:    1 2 3 4 5 6 7 8 9 10    [Next](#)

Free! Google Desktop Search: Search your own computer. [Download now.](#)

**Find:**    emails -  files -  chats -  web history -  media -  PDF

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2005 Google